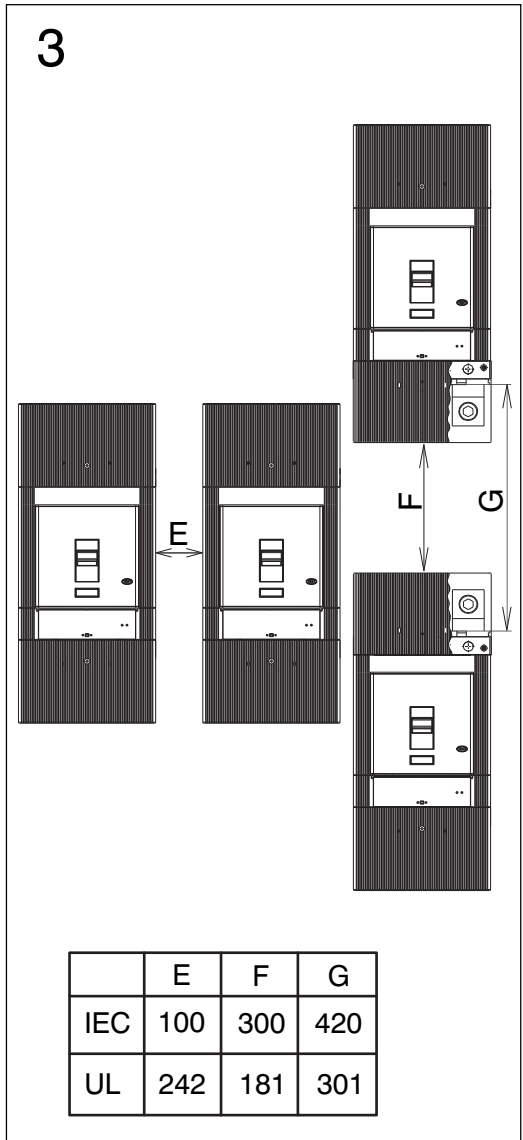
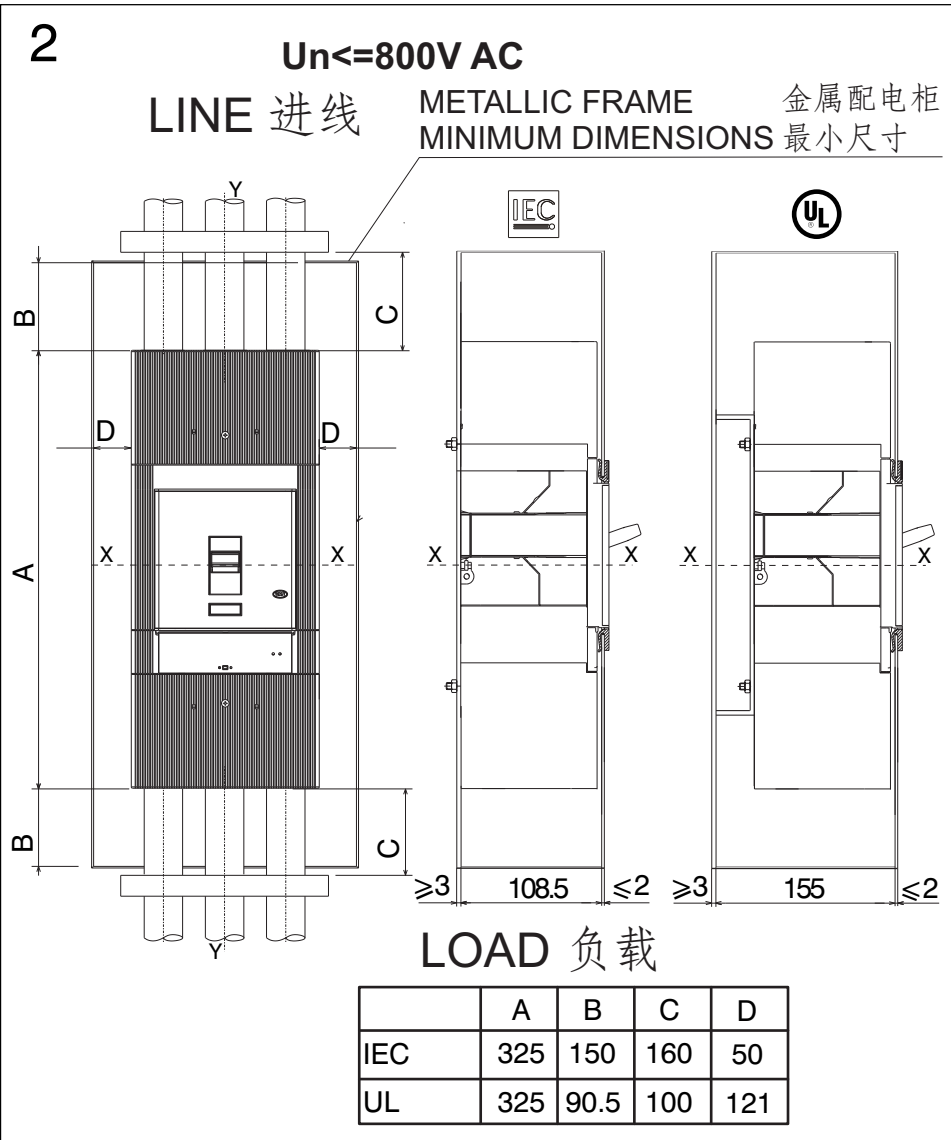
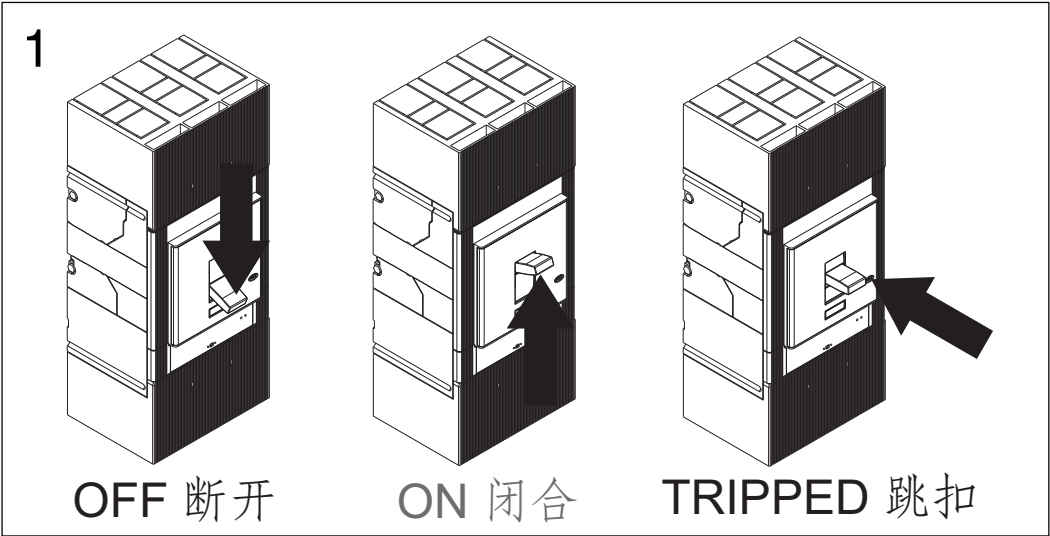
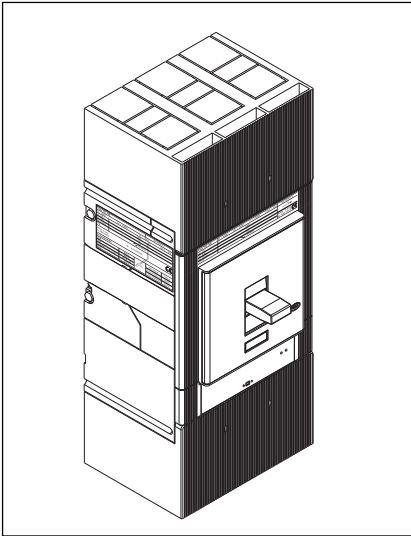
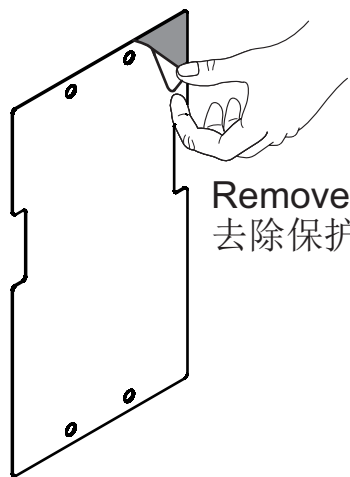


Tmax T5V-HA IEC-UL T5X-HA UL

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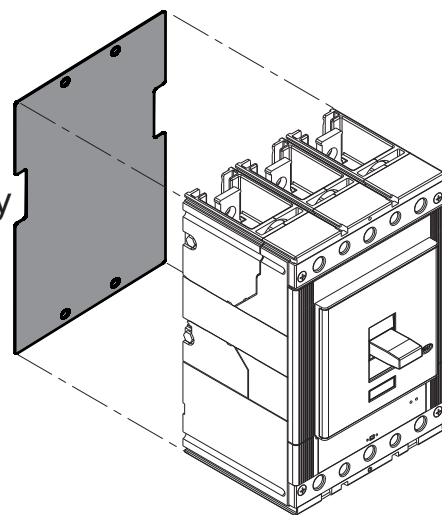


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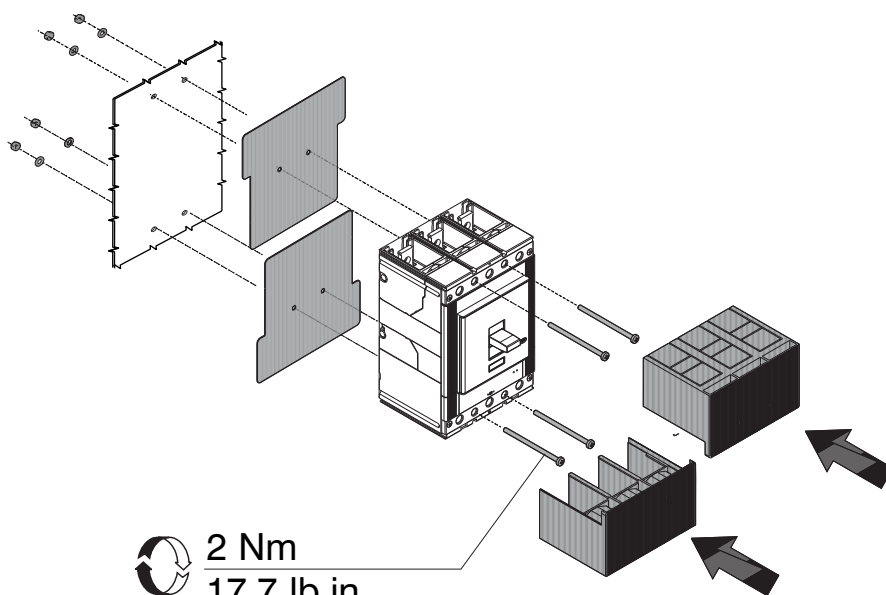


Remove protection
去除保护层

Apply
粘贴

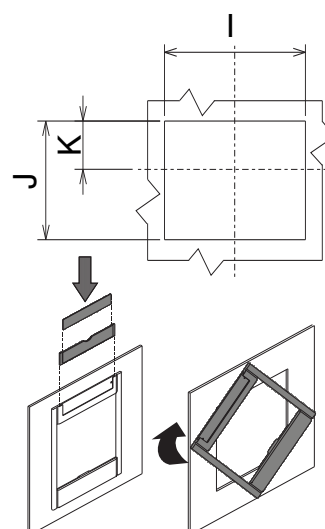
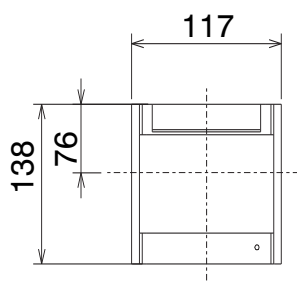
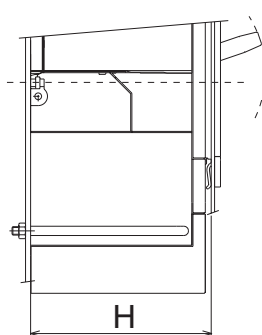


5



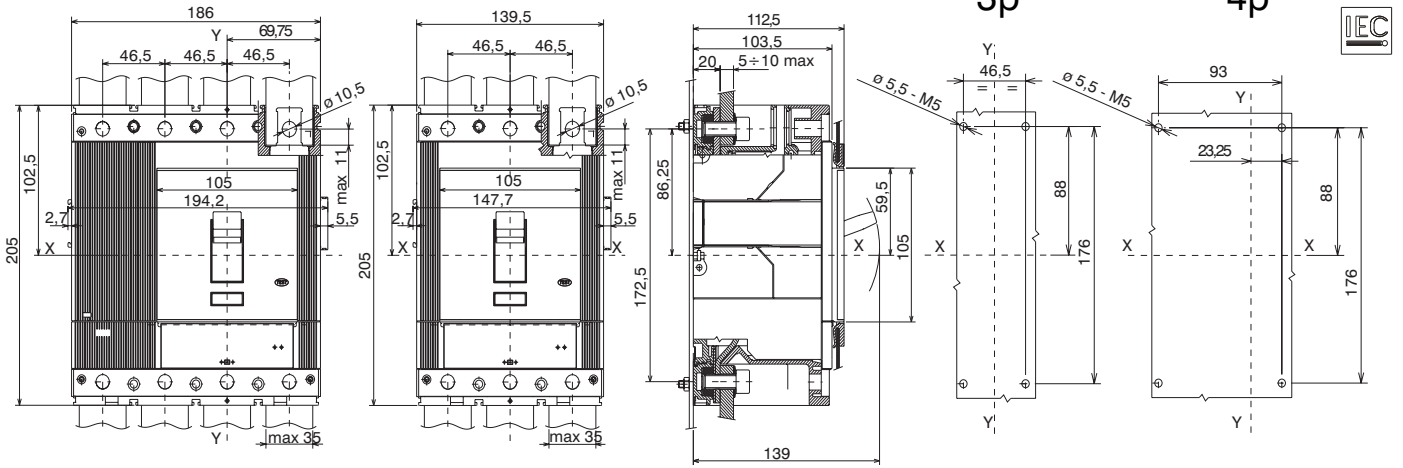
2 Nm
17.7 lb.in

6

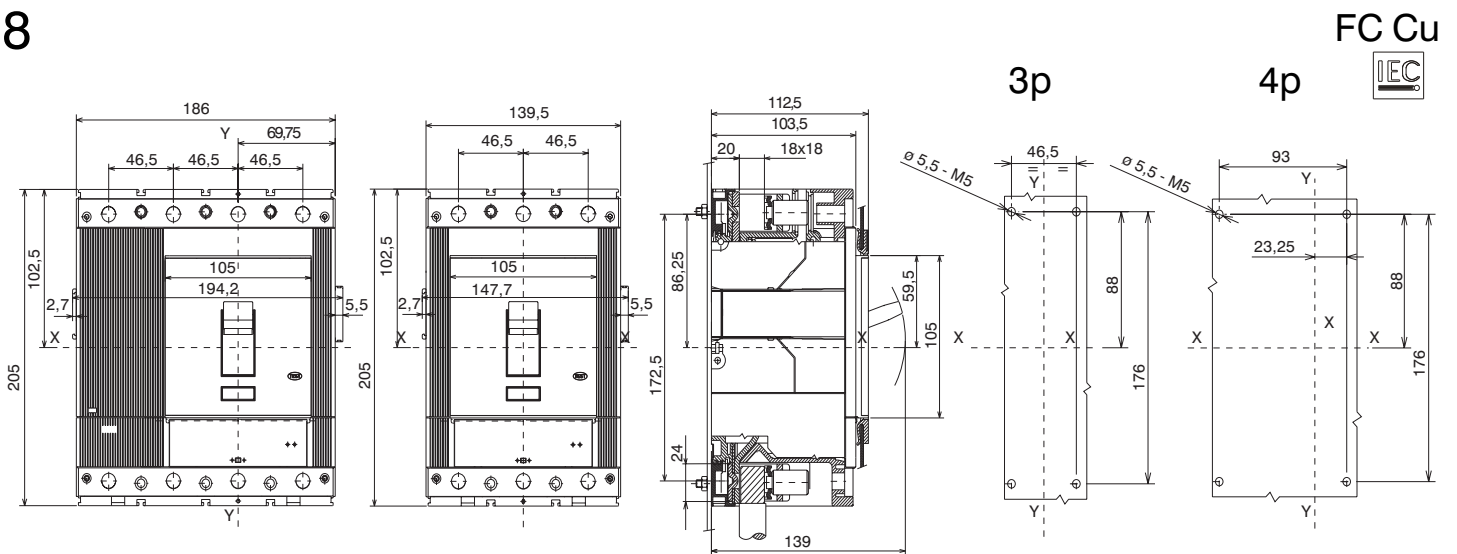


	H	I	J	K
CON MOSTRINA WITH FLANGE 带法兰	108,5	115	115	64,5
SENZA MOSTRINA WITHOUT FLANGE 不带法兰	108,5	107	107	60,5

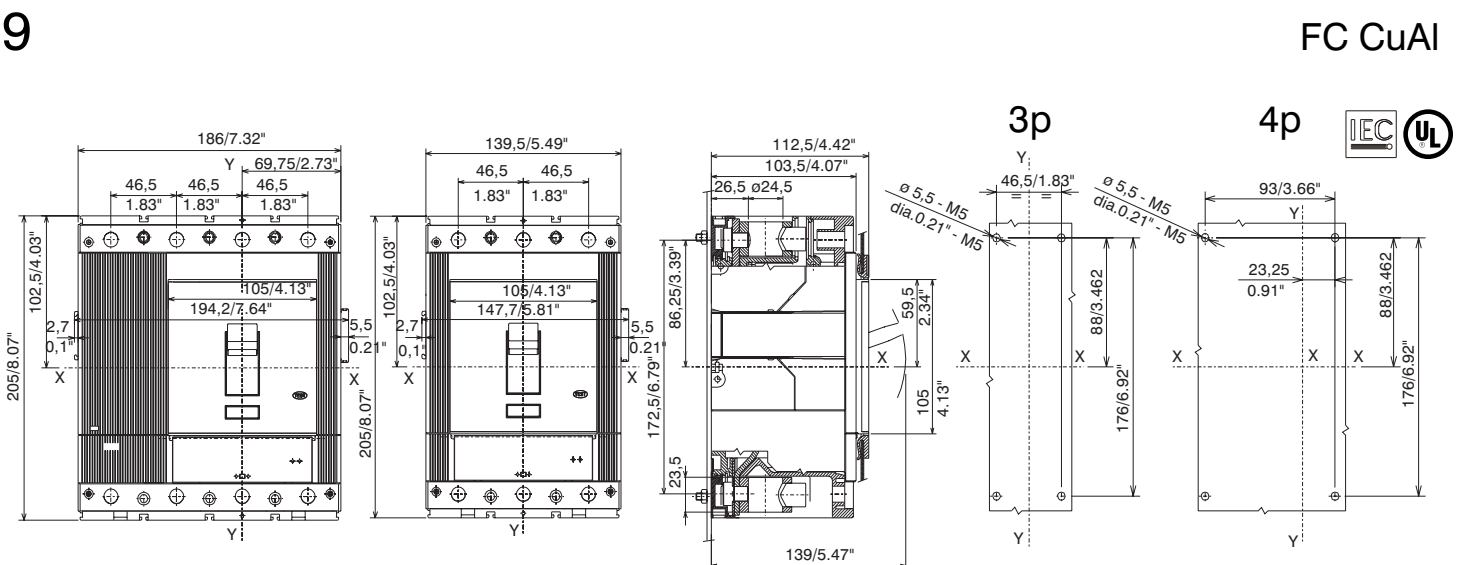
7

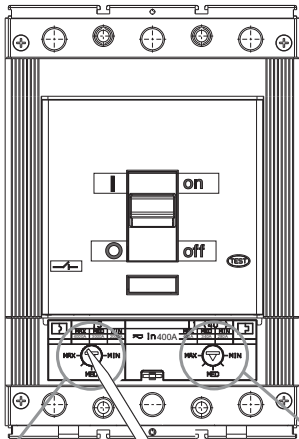
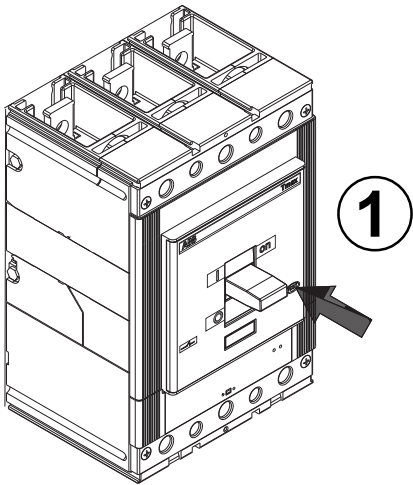


8

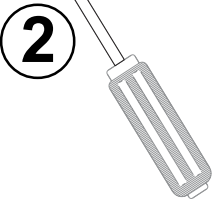


9





I3			
In	MIN 最小值 (In x 5)	MED 中间值 (In x 7,5)	MAX 最大值 (In x 10)
500	2500	3750	5000
400	2000	3000	4000
320	1600	2400	3200



I1			
In	MIN 最小值	MED 中间值	MAX 最大值
500	350	425	500
400	280	340	400
320	224	272	320

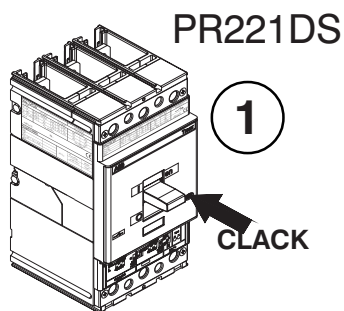


I3			
In	MIN 最小值 (In x 5)	MED 中间值 (In x 7,5)	MAX 最大值 (In x 10)
300	1500	2250	3000

I1			
In	MIN 最小值	MED 中间值	MAX 最大值
300	210	255	300

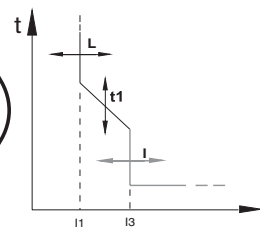
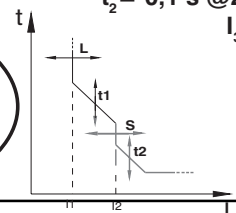


11



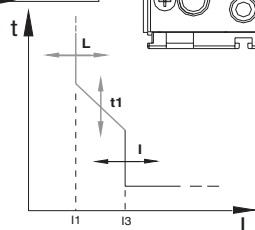
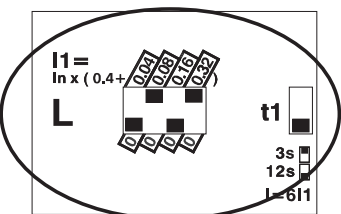
IEC $I_n = 630 A$
 $I_2 = 630 \times (1,5+2) = 2205 A$
 $t_2 = 0,1 s @ 5040 A (8I_n)$
 $I_3 = OFF$

UL $I_n = 300 A$
 $I_2 = 300 \times (1,5+2) = 1050 A$
 $t_2 = 0,1 s @ 2400 A (8I_n)$
 $I_3 = OFF$



IEC $I_n = 630 A$
 $I_3 = 630 \times (1,5+2) = 2205 A$
 $I_2 = OFF$

UL $I_n = 300 A$
 $I_3 = 300 \times (1,5+2) = 1050 A$
 $I_2 = OFF$



IEC $I_n = 630 A$
 $I_1 = 630 \times (0,4+0,08+0,32) = 504 A$
 $t_1 = 12 s @ 3024 A (6I_1)$

UL $I_n = 300 A$
 $I_1 = 300 \times (0,4+0,08+0,32) = 240 A$
 $t_1 = 12 s @ 1800 A (6I_1)$

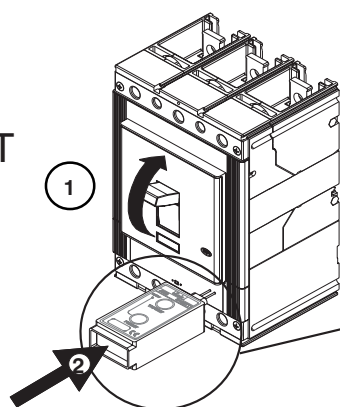
2

Esempio:
 Example:
 例如
 Ne=ON ; 50%

"L" protection provides thermal memory according UL489 requirements

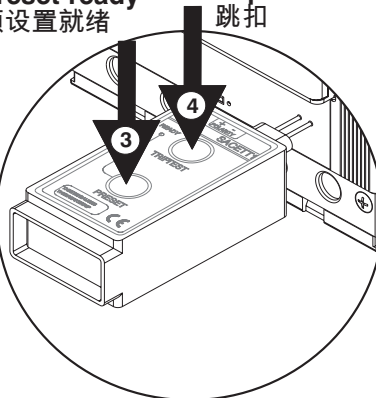
12

TRIP TEST
 跳扣测试



Preset-ready
 预设置就绪

Trip
 跳扣



CLACK

